

### Listing of the Claims

Please cancel claims 9 and 35.

- 5           1. (Original) A system comprising logic configured for:  
counting transitions between data on a bus and data to be put onto the  
bus;  
complementing the data to be put onto the bus where the counted  
transitions exceeds a threshold;  
10          putting complemented data on the bus where the threshold was  
exceeded; and  
putting un-complemented data on the bus where the threshold was not  
exceeded.
- 15          2. (Original) The system as recited in claim 1, wherein the logic  
configured for counting transitions comprises a ripple counter.
- 20          3. (Original) The system as recited in claim 1, wherein the logic  
configured for counting transitions comprises a binary tree.
- 25          4. (Original) The system as recited in claim 1, wherein the logic  
configured for counting transitions comprises a carry look-a-  
head counter.
- 30          5. (Original) The system as recited in claim 1, additionally comprising  
logic configured for setting the threshold at one-half of a width of  
the bus.
6. (Original) The system as recited in claim 1, additionally comprising  
logic configured for setting a signal to indicate complemented  
and un-complemented data.

7. (Original) The system as recited in claim 1, additionally comprising logic configured for setting a signal according to a number of transitions relative to the threshold.

5 8. (Original) The system as recited in claim 1, additionally comprising logic configured for:  
obtaining data from the bus;  
checking a signal to determine if the data has been complemented;  
and  
10 where the data has been complemented, un-complementing the data.

9. (Cancelled).

15 10. (Original) A method of transmitting data, comprising:  
counting transitions between data on a bus and data to be put onto the bus;  
complementing the data to be put onto the bus where a number of transitions exceeds a threshold;  
putting complemented data on the bus where the threshold was  
20 exceeded ; and  
putting un-complemented data on the bus where the threshold was not exceeded.

25 11. (Original) The method as recited in claim 10, wherein counting the transitions comprises counting the transitions using a ripple counter.

30 12. (Original) The method as recited in claim 10, wherein counting the transitions comprises counting the transitions using a binary tree.

13. (Original) The method as recited in claim 10, wherein counting the transitions comprises counting the transitions using a carry look-a-head counter.

14. (Original) The method as recited in claim 10, additionally comprising setting the threshold at one-half of a width of the bus.

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15. (Original) The method as recited in claim 10, additionally comprising setting a signal to indicate complemented and un-complemented data.

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16. (Original) The method as recited in claim 10, additionally comprising:

obtaining data from the bus;

checking a signal to determine if the data has been complemented;

and

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where the data has been complemented, un-complementing the data.

17. (Original) A bus configured for low power consumption and low EMI emissions, comprising:

a transition counter, to count transitions between a first data

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transmission and a second data transmission and to compare

the counted transitions to a threshold;

a data complement module, to complement the second data

transmission where the threshold was exceeded;

a driver circuit to change voltages on data lines from the first data

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transmission to the second data transmission; and

a complement indicator, to operate a signal to indicate if the second data transmission is complemented.

18. (Original) The bus of claim 17, wherein the transition counter comprises a ripple counter.

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19. (Original) The bus of claim 17, wherein the transition counter comprises a binary tree counter.

20. (Original) The bus of claim 17, wherein the transition counter comprises a carry look-a-head counter.

5 21. (Original) The bus of claim 17, wherein output of the transition counter controls operation of the data complement module and the complement indicator.

10 22. (Original) The bus of claim 17, additional comprising:  
a complement detector communicate to monitor the signal line; and  
a data de-complementing module to de-complement the data, if  
indicated by the signal line.

15 23. (Original) The bus of claim 17, additional comprising:  
a complement detector, to detected the signal indicating that the data  
transmitted is complemented.

20 24. (Original) The bus of claim 17, additional comprising:  
a data de-complementing module, to reverse changes made by the  
data complement module.

25 25. (Original) A system for data transmission, comprising:  
means for counting transitions between data on a bus and data to be  
put onto the bus;  
means for complementing the data to be put onto the bus where the  
counted transitions exceeds a threshold;  
means for putting complemented data on the bus where the threshold  
was exceeded; and  
means for putting un-complemented data on the bus where the  
threshold was not exceeded.

30 26. (Original) The system as recited in claim 25, wherein the means for  
counting transitions comprises a binary tree.

27. (Original) The system as recited in claim 25, additionally comprising means for setting the threshold at one-half of a width of the bus.

5 28. (Original) The system as recited in claim 25, additionally comprising means for setting a signal to indicate complemented and un-complemented data.

10 29. (Original) The system as recited in claim 25, additionally comprising means for setting a signal according to a number of transitions relative to the threshold.

30. (Original) A processor-readable medium comprising processor-executable instructions for:

15 counting transitions between data on a bus and data to be put onto the bus;

complementing the data to be put onto the bus where the counted transitions exceeds a threshold;

20 putting complemented data on the bus where the threshold was exceeded; and

putting un-complemented data on the bus where the threshold was not exceeded.

25 31 (Original). The processor-readable medium as recited in claim 30, additionally comprising instructions for setting the threshold at one-half of a width of the bus.

30 32. (Original) The processor-readable medium as recited in claim 30, additionally comprising instructions for setting a signal to indicate complemented and un-complemented data.

33. (Original) The processor-readable medium as recited in claim 30,  
    additionally comprising instructions for:  
obtaining data from the bus;  
checking a signal to determine if the data has been complemented;  
5           and  
where the data has been complemented, un-complementing the data.

34. (Original) The processor-readable medium as recited in claim 30,  
    additionally comprising instructions for:  
10   obtaining data from the bus;  
checking a signal to determine if the data has been complemented;  
          and  
where the data has been complemented, un-complementing the data.

15   35. (Cancelled).